

AMENDMENT

In The Claims:

The following list of claims will replace all prior versions and listings of claims in the application.

The pending claims are listed as follows:

1. (Currently amended) A liquid crystal display panel, comprising:
a thin film transistor array substrate having [[a]] one display region and [[a]] one frame region surrounding said display region, wherein a plurality of pixels are defined in said display region; and
at least [[one]] three color filter layers formed on said display region and said frame region, wherein the portions of said color filter layers located on said frame region are stacked on each other, and the other portions of said color filter layers located on said display region are coplanar and adjacent to each other, and the portions of said color filter layers located on said frame region are used to prevent[[s]] ambient light from projecting onto said frame region and serves as a spacer whereby a cell gap between said thin film transistor array substrate and an opposite substrate is uniformly controlled.
2. (Previously Presented) The liquid crystal display panel according to claim 1, wherein a pattern of a transistor array is formed on said display region.
3. (Previously Presented) The liquid crystal display panel according to claim 1, wherein said cell gap between said thin film transistor array substrate and said opposite substrate is much more uniformly controlled by further forming a planarization layer on said color layer.

4. (Previously Presented) The liquid crystal display panel according to claim 3, wherein said planarization layer is made of a transparent resin.

5. (Previously Presented) The liquid crystal display panel according to claim 1, wherein said liquid crystal display panel is a low temperature polysilicon liquid crystal display panel.

6. (Previously Presented) The liquid crystal display panel according to claim 5, wherein a pattern of a plurality of driving integrated circuits is formed on said frame region.

7. (Previously Presented) The liquid crystal display panel according to claim 1, wherein said color layer is selected from a group consisting of a red color layer, a green color layer, and a blue color layer.

8. (Currently amended) A method of manufacturing a liquid crystal display panel, said liquid crystal display panel including a thin film transistor array substrate having [[a]] one display region and [[a]] one frame region surrounding said display region, wherein a plurality of pixels are defined in said display region, said method comprising the steps of:

(a) respectively and simultaneously forming [[a]] three color filter layers and at least one color layer on said display region and said frame region, wherein the portions of said color filter layers located on said frame region are stacked on each other, and the other portions of said color filter layers located on said display region are coplanar and adjacent to each other;

(b) attaching said thin film transistor array substrate to an opposite substrate to form a space between said thin film transistor array substrate and said opposite substrate; and

(c) injecting a resin made of liquid crystal material into said space.

9. (Original) The method according to claim 8, wherein said step (a) is performed by a photolithography process and a dyeing process.

10. (Original) The method according to claim 8, wherein said step (a) further comprises simultaneously forming a spacer on said display region.

11. (Original) The method according to claim 10, wherein said spacer comprises at least one stacked layer.

12. (Original) The method according to claim 8, wherein after said step (a) further comprises a step of (a1):

forming a planarization layer on said thin film transistor array substrate.

13. (Previously Presented) The method according to claim 12, wherein said planarization layer is made of a transparent resin.

14. (Currently Amended) The method according to claim 12, wherein after said step (a1) further comprises a step of (a2):

forming a transparent resin, and then polishing the transparent resin ~~polishing said planarization layer by chemical-mechanical polishing to a pre-determined thickness to form the planarization layer.~~

15. (Original) The method according to claim 12, wherein after said step (a1) further comprises a step of (a3):

uniformly spraying a plurality of plastic beads on said display region.

16. (Original) The method according to claim 15, wherein said plastic beads control a cell gap between said thin film transistor array substrate and said opposite substrate.

17. (Original) The method according to claim 8, wherein a pattern of a transistor array is formed on said display region.

18. (Original) The method according to claim 8, wherein a transparent conducting electrode is formed on said opposite substrate.

19. (Original) The method according to claim 18, wherein said transparent conducting electrode is made of indium tin oxide.

20. (Currently Amended) A liquid crystal display panel, comprising:
a thin film transistor array substrate having [[a]] one display region and [[a]] one frame region surrounding said display region, wherein a plurality of pixels are defined in said display region; and

three [[a]] color layers formed on said frame region, wherein the portions of said color filter layers located on said frame region are stacked on each other, and the other portions of said color filter layers located on said display region are coplanar and adjacent to each other, and the portions of said filter color layers located on the frame region are used to prevent[[s]] ambient light from projecting onto said frame region and serves as a spacer whereby a cell gap between said thin film transistor array substrate and an opposite substrate is uniformly controlled, wherein the [[three]] color [[filter]] layers is two-layered and comprises two layers selected from a combination of a red color layer, a green color layer, and a blue color layer, or the color layer is three-layered and comprise[[s]] a red color layer, a green color layer, and a blue color layer.